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IN RE PATENT APPLICATION OF:

**RAMBAUD**

TITLE: **DIGITAL ADAPTIVE EQUALIZER FOR T1/E1 LONG HAUL TRANSCEIVER**

April 4, 2002

**SUBMISSION OF FORMAL DRAWINGS**

Director of the US Patent & Trademark Office  
Washington, DC 20231

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APR 08 2002

Technology Center 2600

Sir:

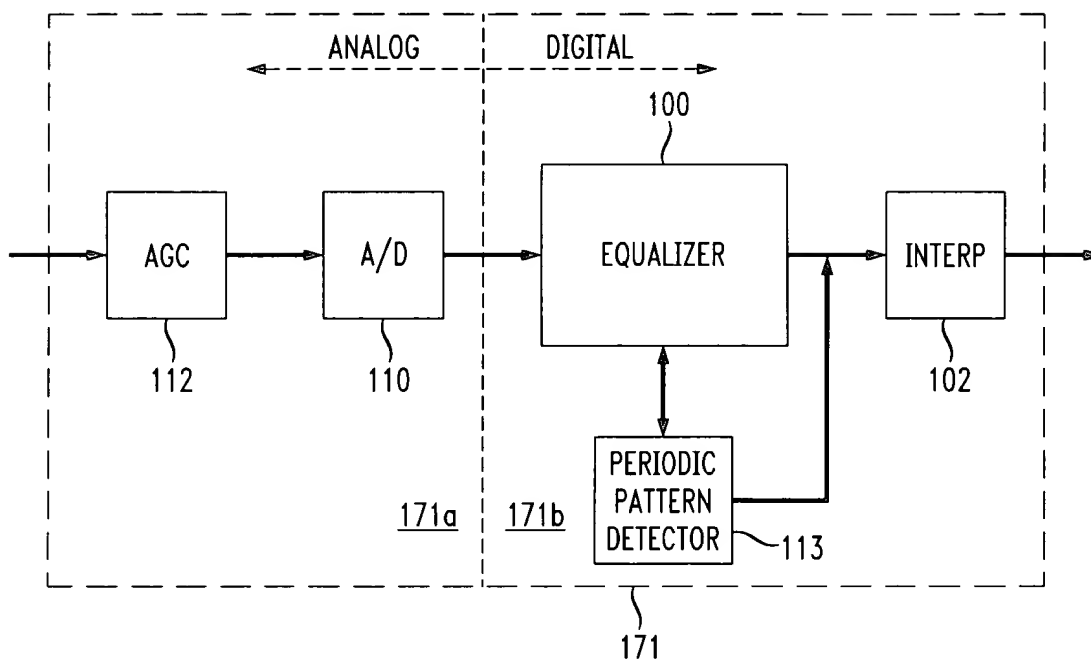
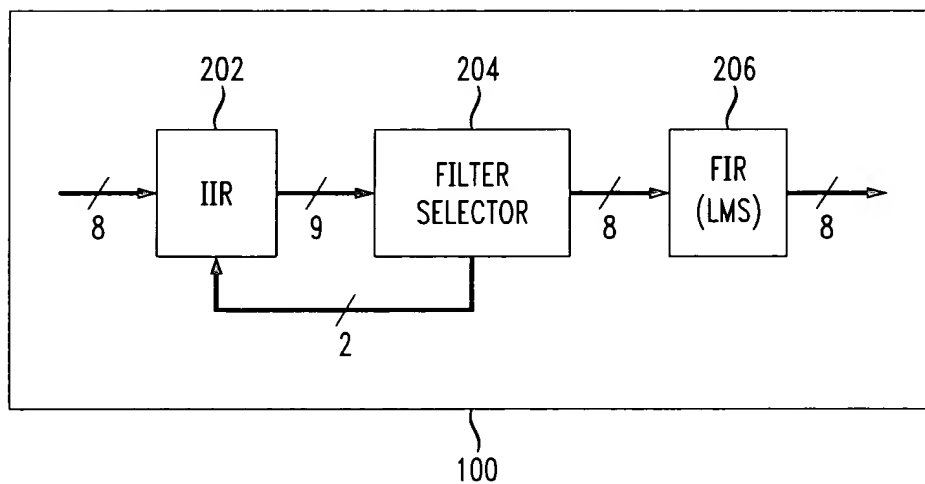
Please accept the herewith 8 sheets of formal drawings on A4 size paper of Figures 1 through 9, which are in lieu of the informal drawings filed earlier.

Respectfully submitted,

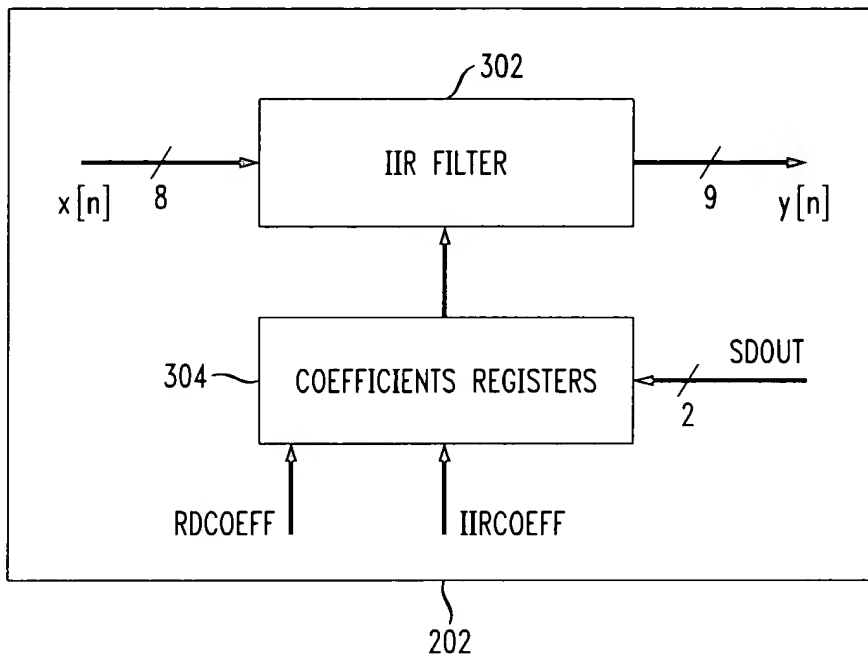
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1/8

*FIG. 1*GENERAL FRONT END ARCHITECTURE*FIG. 2*EQUALIZER ARCHITECTURE

2/8

*FIG. 3*IIR FILTER BLOCK DESCRIPTION



4/8

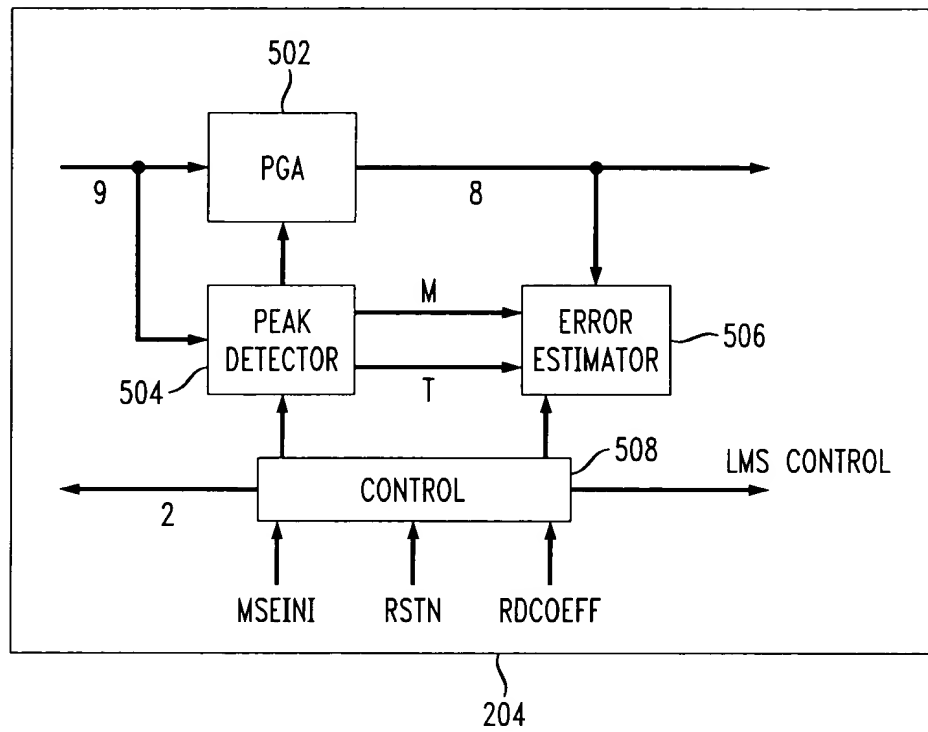
*FIG. 5*FILTER SELECTION BLOCK DESCRIPTION

FIG. 6

## FILTER SELECTION BLOCK IMPLEMENTATION

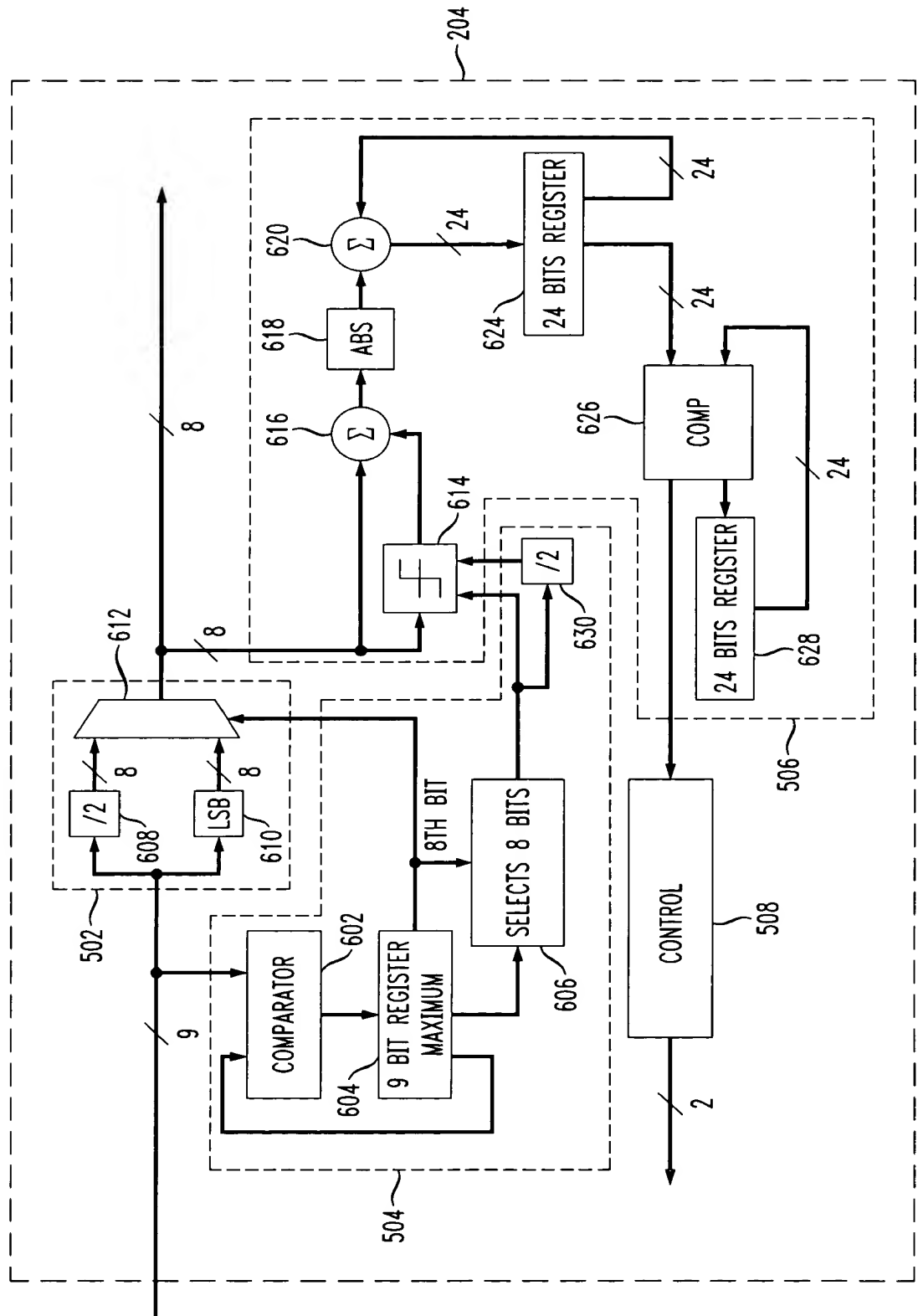
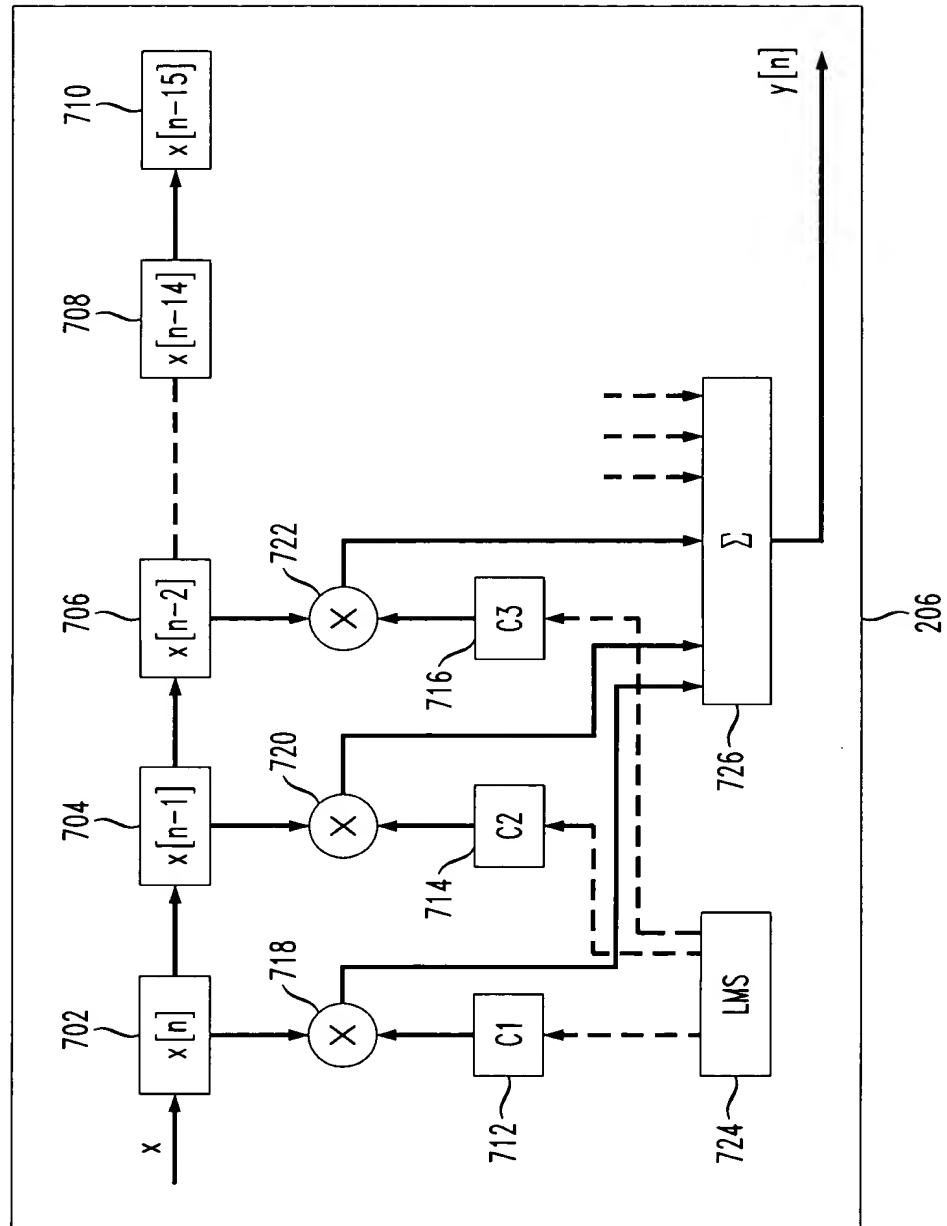


FIG. 7

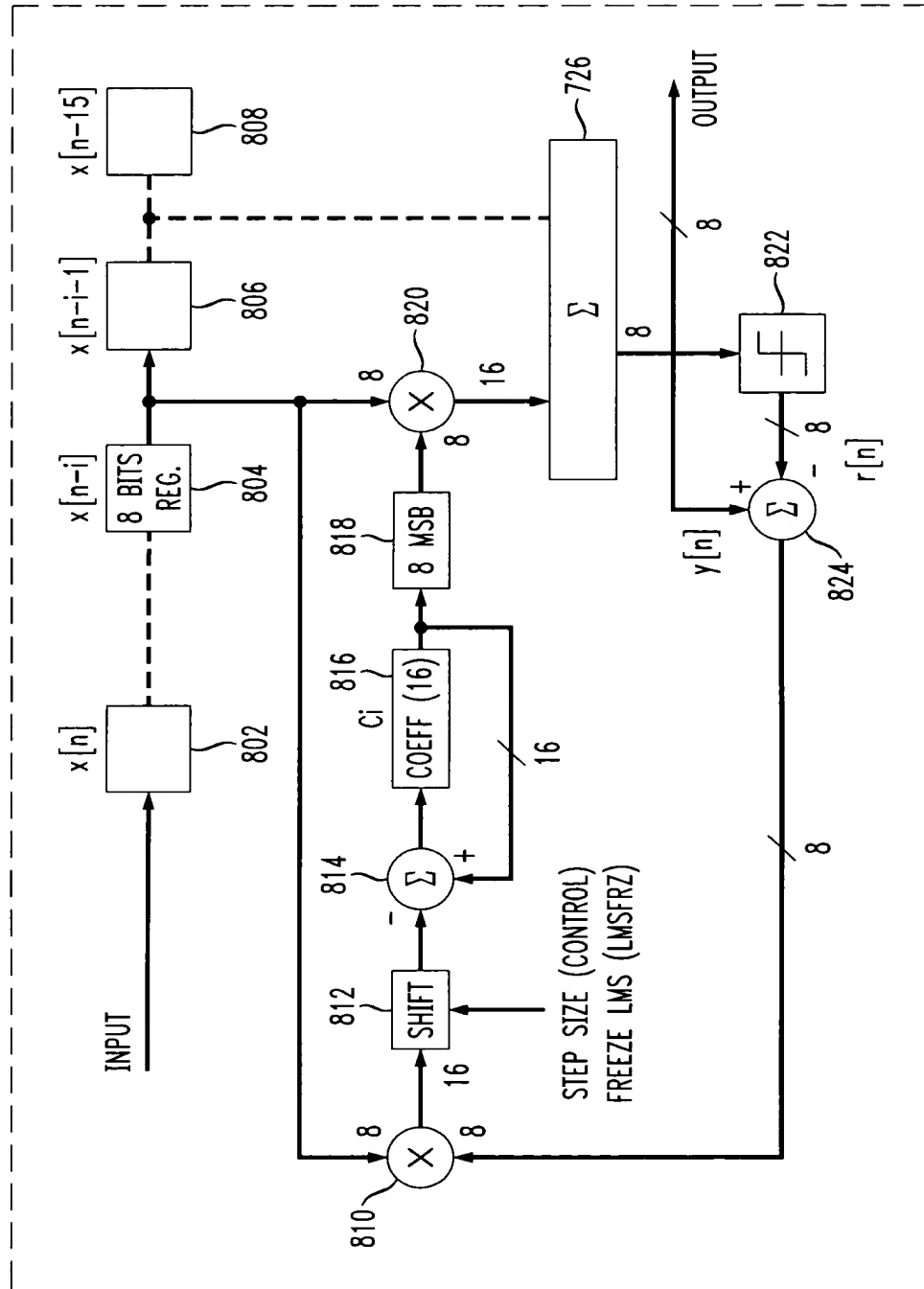
## ADAPTIVE FIR FILTER DESCRIPTION



7/8

FIG. 8

## ADAPTIVE FIR FILTER BLOCK IMPLEMENTATION



8/8

*FIG. 9*  
PRIOR ART

